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DATE MAILED: 07/12/2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/967,187	09/28/2001	Gregory E. Howard	TI-29894	5978
23494	7590 07/12/2004		EXAMINER	
TEXAS INSTRUMENTS INCORPORATED			SOWARD, IDA M	
P O BOX 653 DALLAS, T	5474, M/S 3999 X 75265			PAPER NUMBER
DALLERO, 1	1 15205	,	2822	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application N .	Applicant(s)	UIC			
	09/967,187	HOWARD ET AL.				
Offic Action Summary	Examiner	Art Unit				
	Ida M Soward	2822				
The MAILING DATE f this communication ap Period for Reply	pears on the cover sheet wi	th th corresp ndenc address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repleted in the provision of the pro	136(a). In no event, however, may a most of the statutory minimum of thirt is will apply and will expire SIX (6) MON te, cause the application to become AB	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communic ANDONED (35 U.S.C. § 133).	cation.			
Status						
1) Responsive to communication(s) filed on 29.	January 2004.					
	is action is non-final.					
3) Since this application is in condition for allowa						
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D	. 11, 453 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-9 is/are pending in the application.						
4a) Of the above claim(s) is/are withdra	awn from consideration.					
5)⊠ Claim(s) <u>5-9</u> is/are allowed.						
6)⊠ Claim(s) <u>1 and 4</u> is/are rejected.						
7)⊠ Claim(s) <u>2-3</u> is/are objected to.	Claim(s) <u>2-3</u> is/are objected to.					
8) Claim(s) are subject to restriction and/	or election requirement.					
Application Papers						
9)☐ The specification is objected to by the Examin	er.					
10)☐ The drawing(s) filed on is/are: a)☐ ac	cepted or b)☐ objected to	by the Examiner.				
Applicant may not request that any objection to the	e drawing(s) be held in abeyan	ce. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correct	,	· · · · · ·				
11) The oath or declaration is objected to by the E	examiner. Note the attached	Office Action or form PTO-15	2.			
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of:		119(a)-(d) or (f).				
1. Certified copies of the priority documen						
2. Certified copies of the priority documen		· ·				
3. Copies of the certified copies of the price	·	received in this National Stage	;			
application from the International Burea	` ' ''					
* See the attached detailed Office action for a lis	t of the certified copies not	receivea.				

U.S. Patent and Trademark Office PTOL-326 (Rev. 1-04)

1) Notice of References Cited (PTO-892)

Paper No(s)/Mail Date _

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)

Attachment(s)

5) Notice of Informal Patent Application (PTO-152)

6) Other: ____.

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DETAILED ACTION

This Office Action is in response to the Applicants' amendment filed January 29, 2004.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Momose (5,198,692).

Momose teaches a bipolar junction transistor, comprising: a buried collector layer 2; a counterdoped collector region 3 (counterdoped with a P-type dopant to form 4) adjacent to the buried collector layer; a base region 4 adjacent to the counterdoped collector region; and an emitter region 5 adjacent to the base region (Figures 1A-1C).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Momose (5,198,692) in view of Kovacic et al. (US 6,346,453 B1).

Momose teaches all mentioned in the rejection above. However, Momose fails to teach a SiGe base region. Kovacic et al. teach a SiGe base region. Since Momose and Kovacic et al. are from the same field on endeavor (bipolar junction transistors), the purpose disclosed by Kovacic et al. would have been recognized in the pertinent art of Momose Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the BJT structure of Momose with the BJT structure having a SiGe base as taught by Kovacic et al. to inject charge carriers with greater efficiency (cols. 1-2, lines 59-67 and 1-5, respectively).

Allowable Subject Matter

Claims 5-9 are allowed.

Claims 2-3 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents are cited to further show the state of the art with respect to bipolar junction transistors:

Carroll et al. (6,001,701)

DeJong et al. (5,311,054)

Lien (5,652,456)

Lloyd (3,638,081)

Mastroianni (5,117,274)

Spratt et al. (5,065.209)

Tsai et al. (US 2002/0105054 A1).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ida M Soward whose telephone number is 571-272-1845. The examiner can normally be reached on Monday - Thursday, 6:30 am to 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

IMS June 13, 2004

ÁMIR ZARABIAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800